MAC

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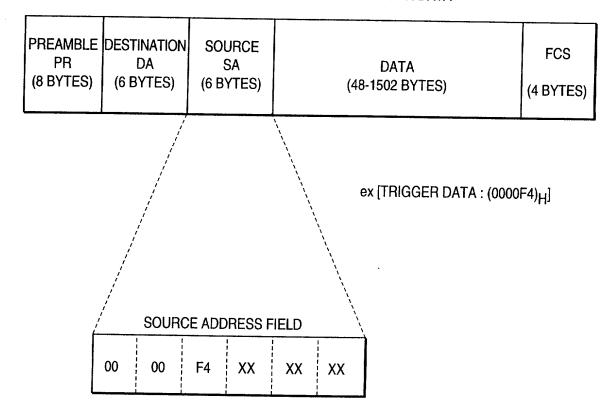
S

OPTICAL FIBER CABLE 100BASE-FX MEDIA CONVERTER (MC)10 8-BIT MICROPROCESSOR 104 짪 102 FIFO <u>P</u> 103 105 ELB ₽H 101 RESPONSE PACKET
PRPL UTPCABLE 100BASE-TX Ртяс TRIGGER PACKET 203 201 MAC SPU ₽HY

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FIG. 2

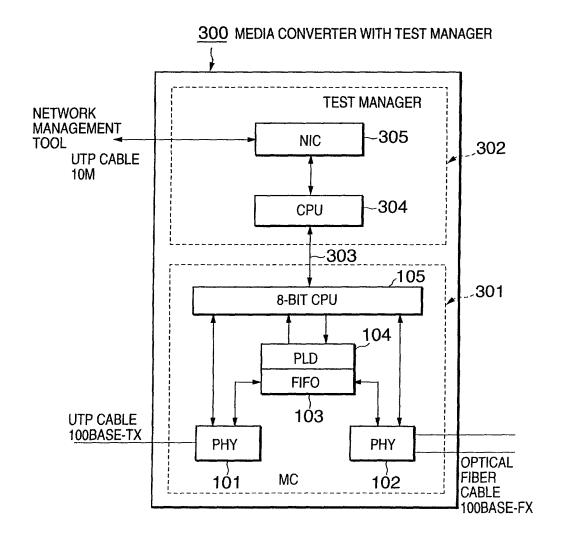
ETHERNET PACKET INCLUDING TRIGGER DATA

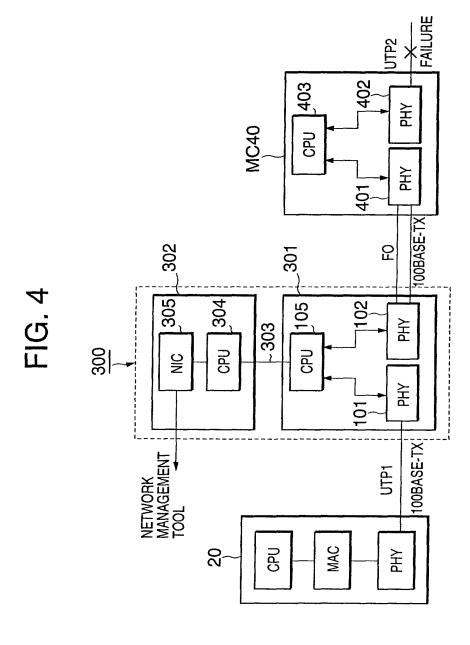


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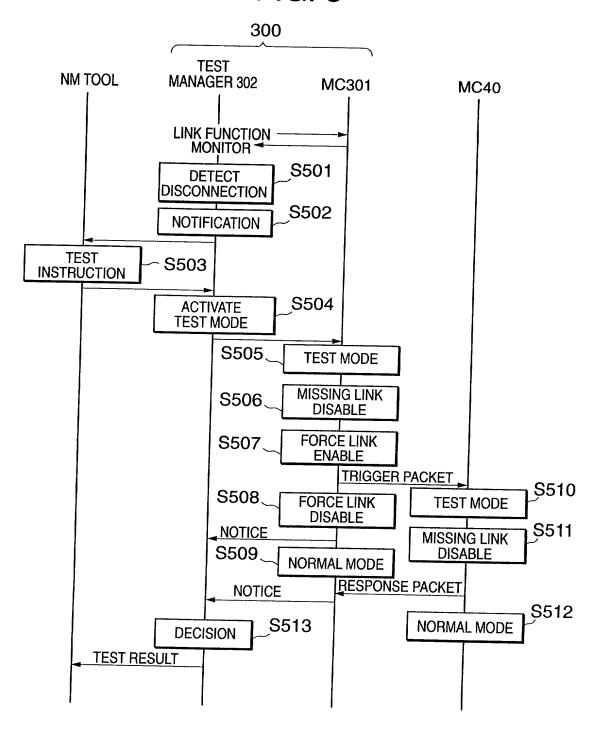
FIG. 3





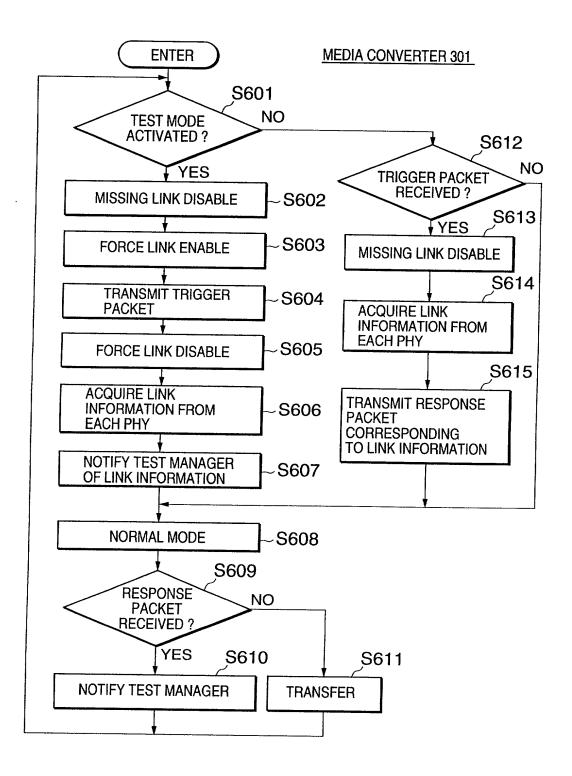
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FIG. 5



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FIG. 6



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FIG. 7

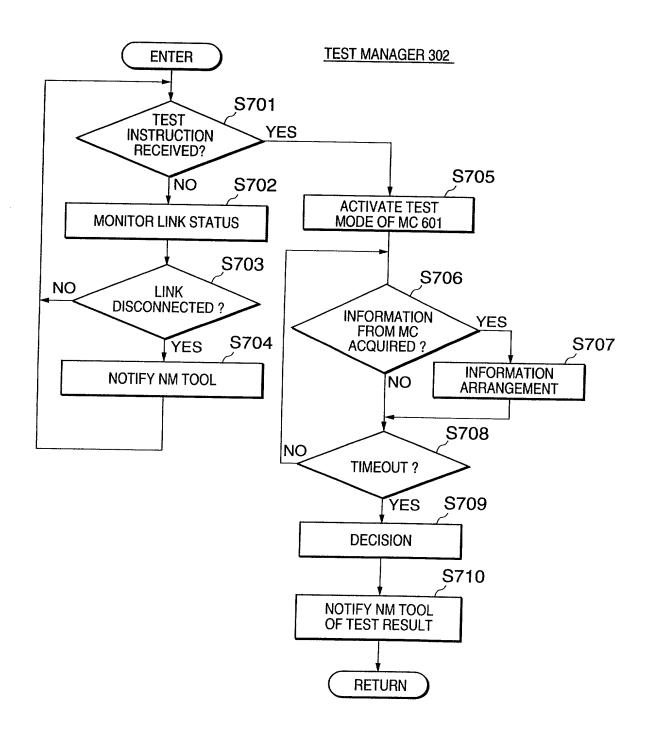


FIG. 8

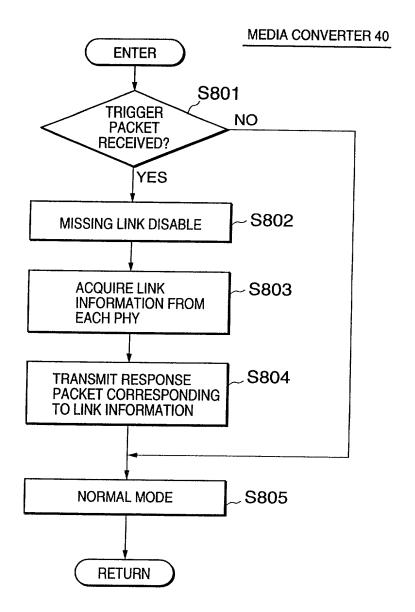
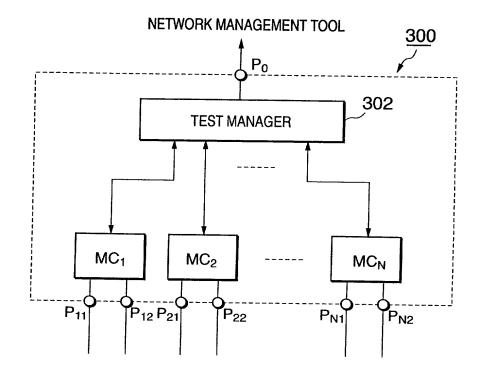
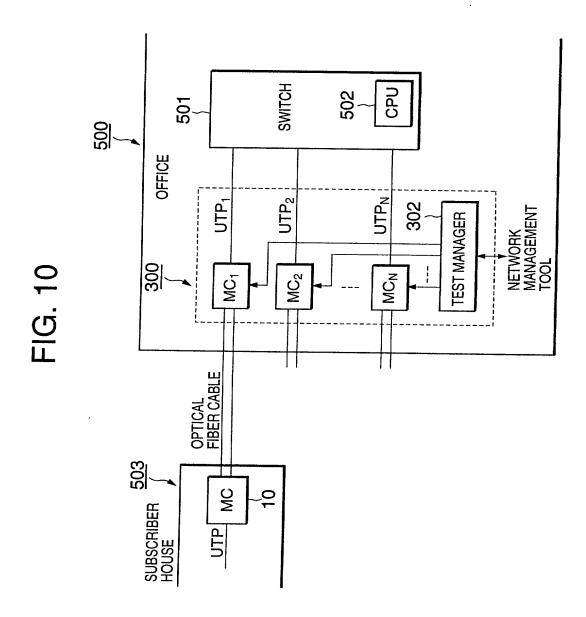


FIG. 9



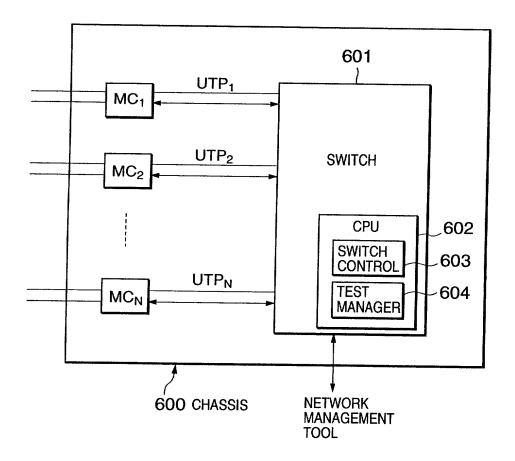
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FIG. 11



- OPTICAL - FIBER

2 \mathbb{H} ₹ 707 704 **202** FIG. 12 8-BIT CPU 댎 8 PACKET-BUFFER-EQUIPPED MEDIA CONVERTER 703 702 PACKET BUFFER ${\rm PHY}$ ≣ 표 Ę

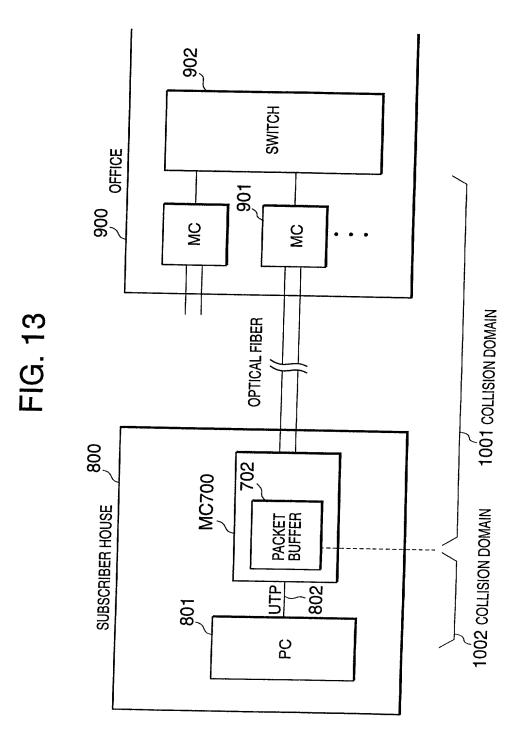


FIG. 14A

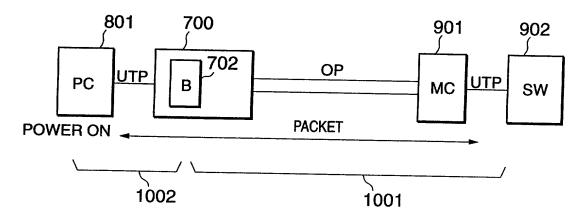


FIG. 14B

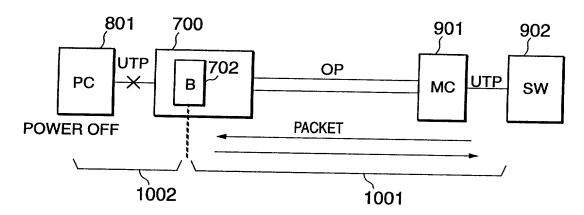


FIG. 14C (PRIOR ART)

